

L Number	Hits	Search Text	DB	Time stamp
	402	370/376.cccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:20
	171	370/375.cccls.	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:20
	201	370/378.cccls.	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:20
	155	370/379.cccls.	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:20
	153	370/370.cccls.	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:21
	1884	370/503.cccls.	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:21
	107	370/369.cccls.	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:22
	36950	TDM or (time adj division adj multiplex\$)	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:23
	70977	(circuit adj switch\$)	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:23
	4280	TSI or (time adj slot adj interchange)	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:34
	1249	frame\$2 and subframe\$2 and clock	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:25
	70645	address same counter	US-PGPUB; EPO; JPO; DERWENT; IBM TDB USPAT;	2004/08/19 10:25
	27452	(RAM or (random with access adj3 memory)) with buffer\$2	US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 10:37

		15	370/376.ccls. and (TSI or (time adj slot adj interchange)) and (address same counter) and ((RAM or (random with access adj3 memory)) with buffer\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 15:27
		119	(TSI or (time adj slot adj interchange)) and (address same counter) and ((RAM or (random with access adj3 memory)) with buffer\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 10:35
		8	(TSI or (time adj slot adj interchange)) and (address same counter) and ((RAM or (random with access adj3 memory)) with buffer\$2) and (frame\$2 and subframe\$2 and clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 10:28
		1	370/376.ccls. and (TSI or (time adj slot adj interchange)) and (address same counter) and ((RAM or (random with access adj3 memory)) with buffer\$2) and (frame\$2 and subframe\$2 and clock)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 10:33
		4473	TSI or (time adj slot\$2 adj interchange\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 10:35
		133	(TSI or (time adj slot\$2 adj interchange\$2)) and (address same counter) and ((RAM or (random with access adj3 memory)) with buffer\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 10:35
		119	(TSI or (time adj slot\$2 adj interchange\$2)) and (TSI or (time adj slot adj interchange)) and (address same counter) and ((RAM or (random with access adj3 memory)) with buffer\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 10:36
		15	(TSI or (time adj slot\$2 adj interchange\$2)) and 370/376.ccls. and (address same counter) and ((RAM or (random with access adj3 memory)) with buffer\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 10:36
		39261	(RAM or (random with access adj3 memory)) same buffer\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 15:25
		48	(TSI or (time adj slot\$2 adj interchange\$2)) and 370/376.ccls. and (address same counter)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 10:37
		154054	(RAM or (random with access adj3 memory) or storage or table or arrary) same buffer\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 15:26
		23	370/376.ccls. and (TSI or (time adj slot adj interchange)) and (address same counter) and ((storage or (memory)) with buffer\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 15:30
		9	370/376.ccls. and (TSI or (time adj slot adj interchange)) and (address same counter) and ((storage or (memory)) with buffer\$2) and stage\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 15:31
		91	370.clas. and (TSI or (time adj slot adj interchange)) and (address same counter) and ((storage or (memory)) with buffer\$2) and stage\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 15:35

	140	370.clas. and (TSI or (time adj slot adj interchange)) and (address same counter) and ((storage or (memory))) and stage\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:36
	233	370.clas. and (TSI or (time adj slot adj interchange)) and ((storage or (memory))) and TDM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:42
	728	370.clas. and (TSI or (time adj slot adj interchange)) and ((storage or (memory)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:39
	683	370.clas. and (TSI or (time adj slot adj interchange\$2)) and ((storage or (memory))) and frame\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:40
	803	370.clas. and (TSI or (time adj slot adj interchange\$2)) and ((storage or (memory)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 16:09
	607	370.clas. and (TSI or (time adj slot adj interchange)) and ((storage or (memory))) and frame	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:41
	254	370.clas. and (TSI or (time adj slot adj interchange\$2)) and ((storage or (memory))) and TDM	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:42
	803	370.clas. and (TSI or (time adj slot adj interchange\$2)) and ((storage or (memory)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:43
	55	370.clas. and (TSI or (time adj slot adj interchange\$2)) and ((storage or (memory))) and frame\$2 and subframe\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:48
	69	370.clas. and (TSI or (time adj slot adj interchange\$2)) and ((storage or (memory))) and (digital adj cross adj connect\$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 15:48
	371	(TSI or (time adj slot adj interchange\$2)) and ((storage or (memory))) and stages and frame\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 16:10
	21	(TSI or (time adj slot adj interchange\$2)) and ((storage or (memory))) and stages and frame\$2 and subframe\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 16:17
	138	(TSI or (time adj slot adj interchange\$2)) and ((storage or (memory))) and (switch\$2 same stages)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 16:18
	115	(TSI or (time adj slot adj interchange\$2)) and ((storage or (memory))) and (switch\$2 same stages) and frame\$2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/08/19 16:18

	12	(TSI or (time adj slot adj interchange\$2)) and ((storage or (memory))) and (switch\$2 same stages) and (subframe\$2 with frame\$2)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 16:23
	9	(TSI or (time adj slot adj interchange\$2)) and ((storage or (memory)) same address) and (switch\$2 same stages) and (subframe\$2 with frame\$2) and (read or write)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 16:24
	4	(TSI or (time adj slot adj interchange\$2)) and ((storage or (memory)) same address) and (multistage\$2) and (subframe\$2 with frame\$2) and (read or write) and counter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 16:56
	8	(TSI or (time adj slot adj interchange\$2)) and ((storage or (memory)) same address) and (switch\$2 same stages) and (subframe\$2 with frame\$2) and (read or write) and counter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 17:06
	36	(TSI or (time adj slot adj interchange\$2)) and ((storage or (memory)) same address) and (subframe\$2 same frame\$2) and ((read or write) with address)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 17:07
	35	(TSI or (time adj slot adj interchange\$2)) and ((storage or (memory)) same address) and (subframe\$2 same frame\$2) and ((read or write) with address) and clock	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/08/19 17:07